

FIG. 1

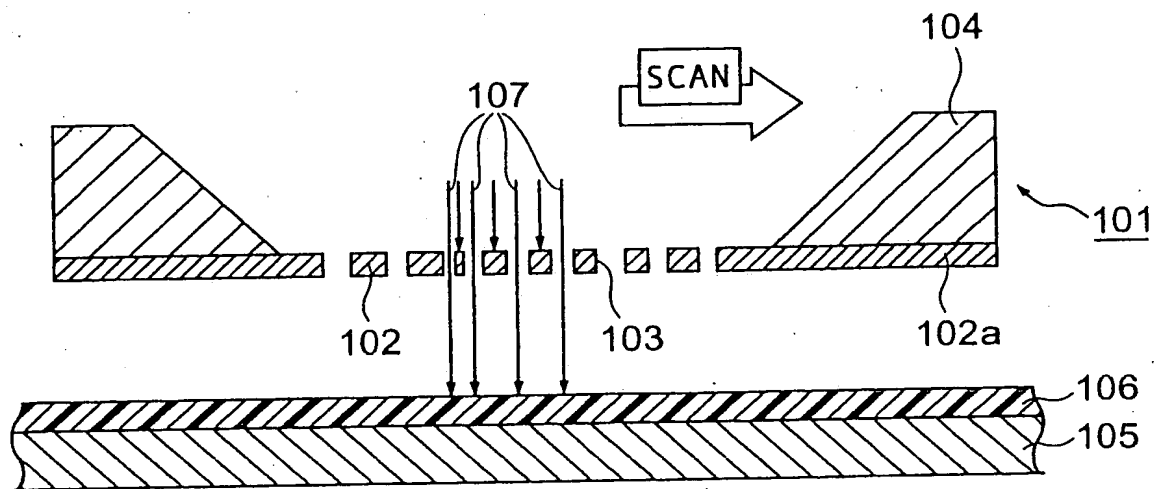


FIG. 2

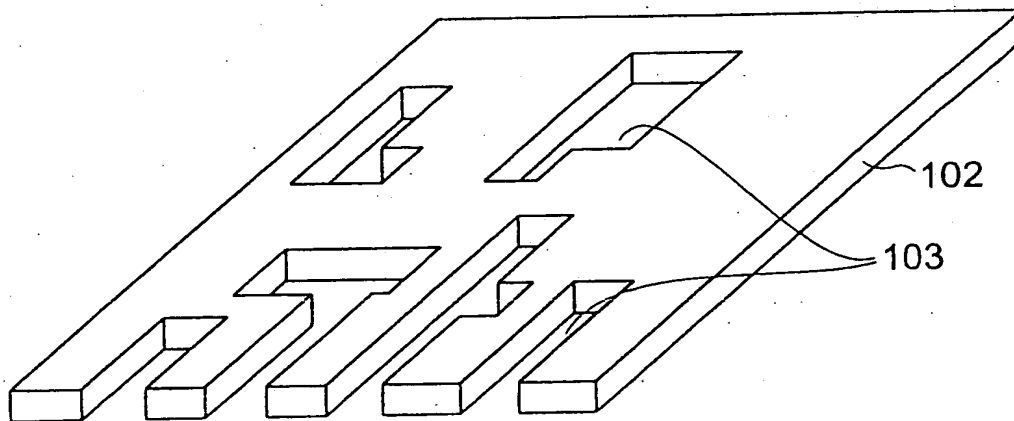


FIG. 3

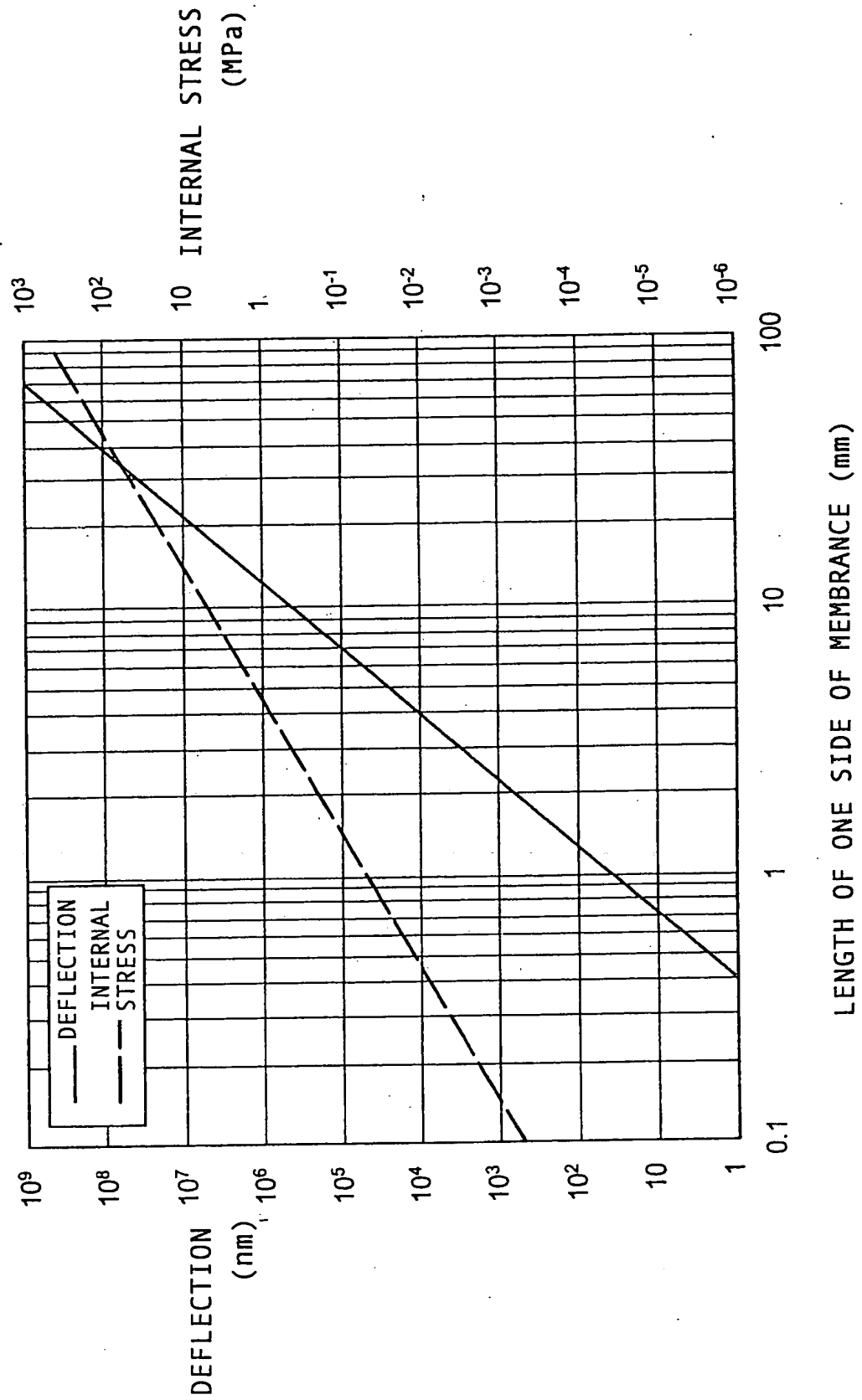


FIG. 4

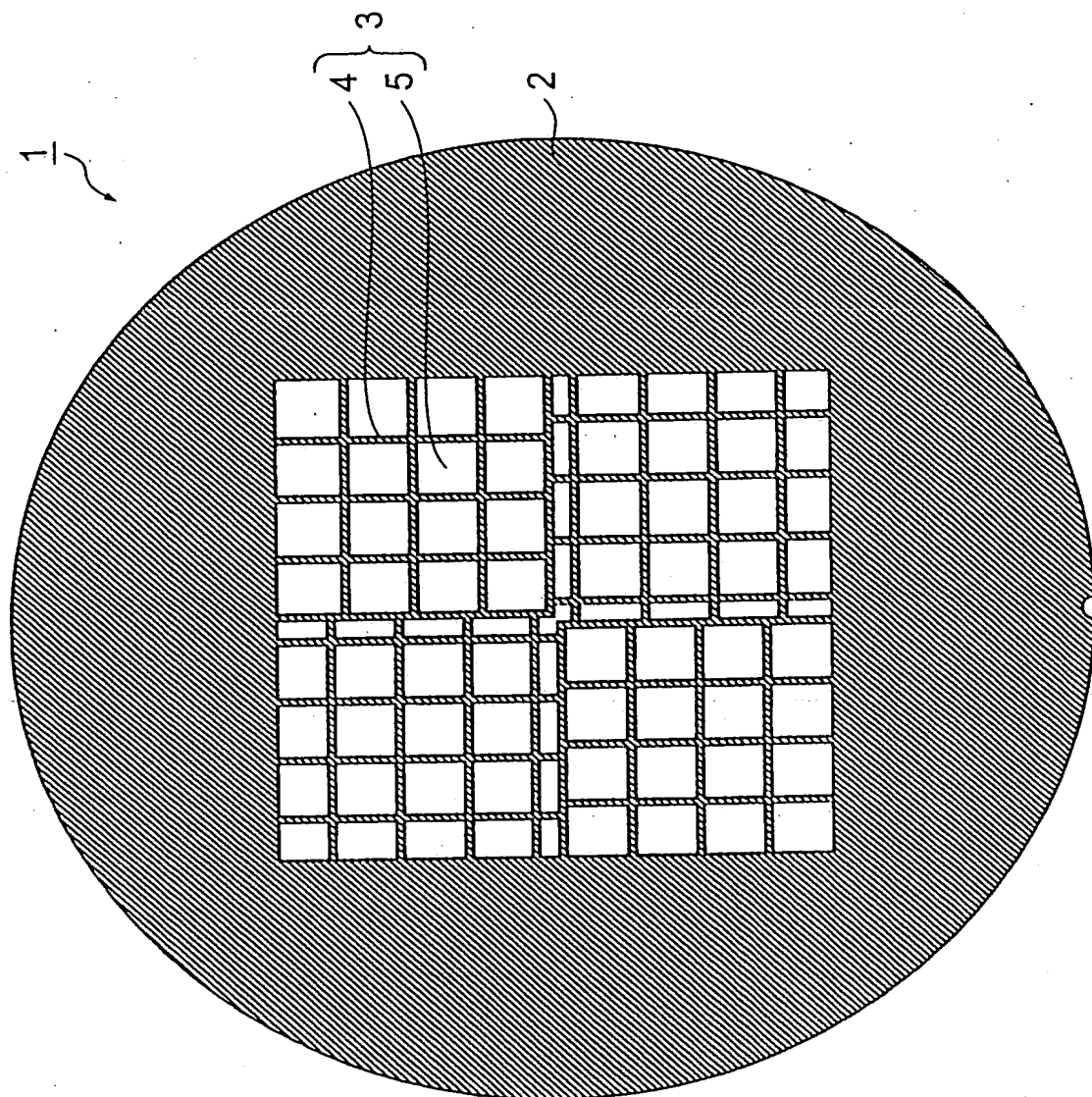


FIG. 5

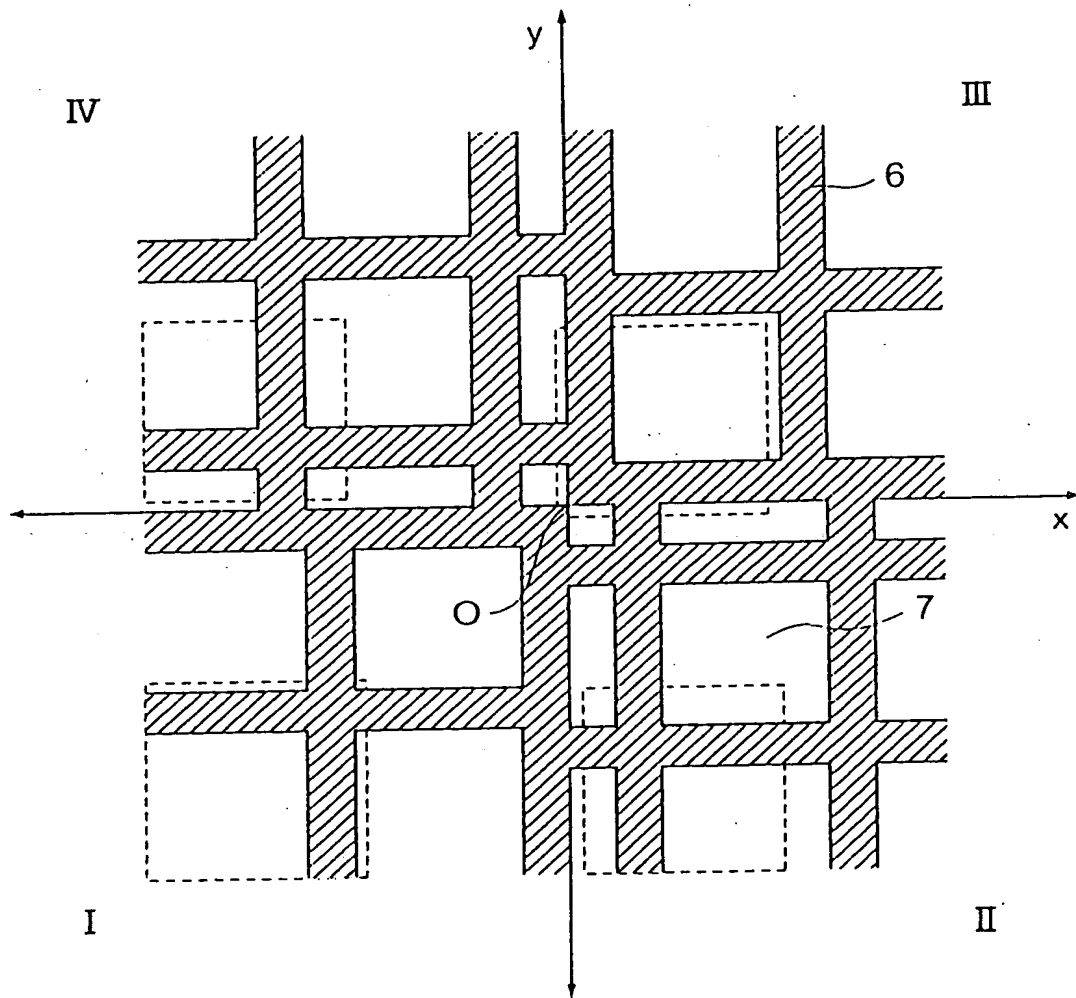


FIG. 6

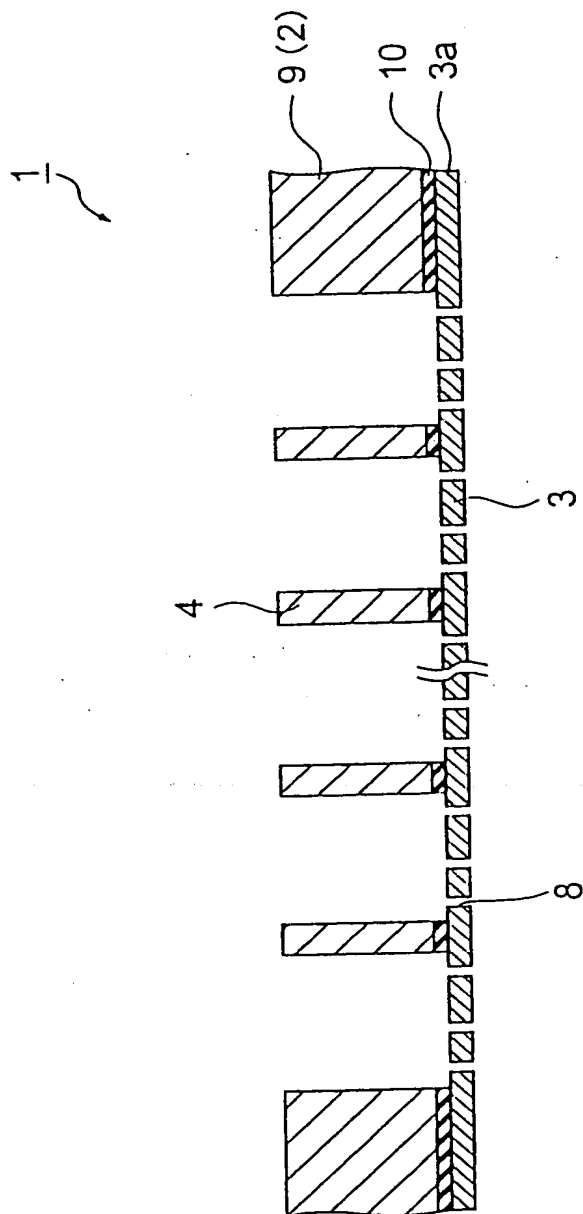


FIG. 7

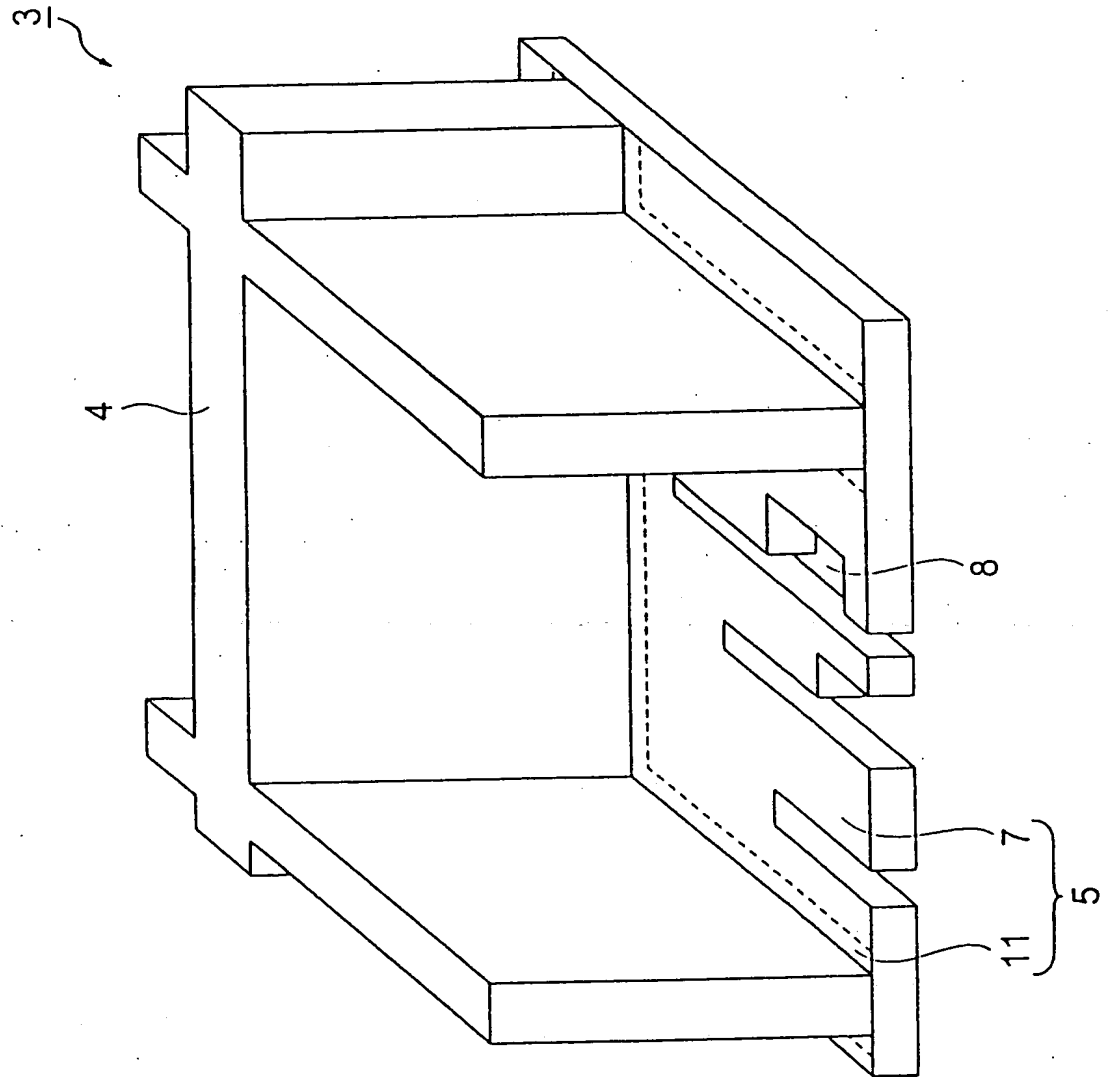


FIG. 8

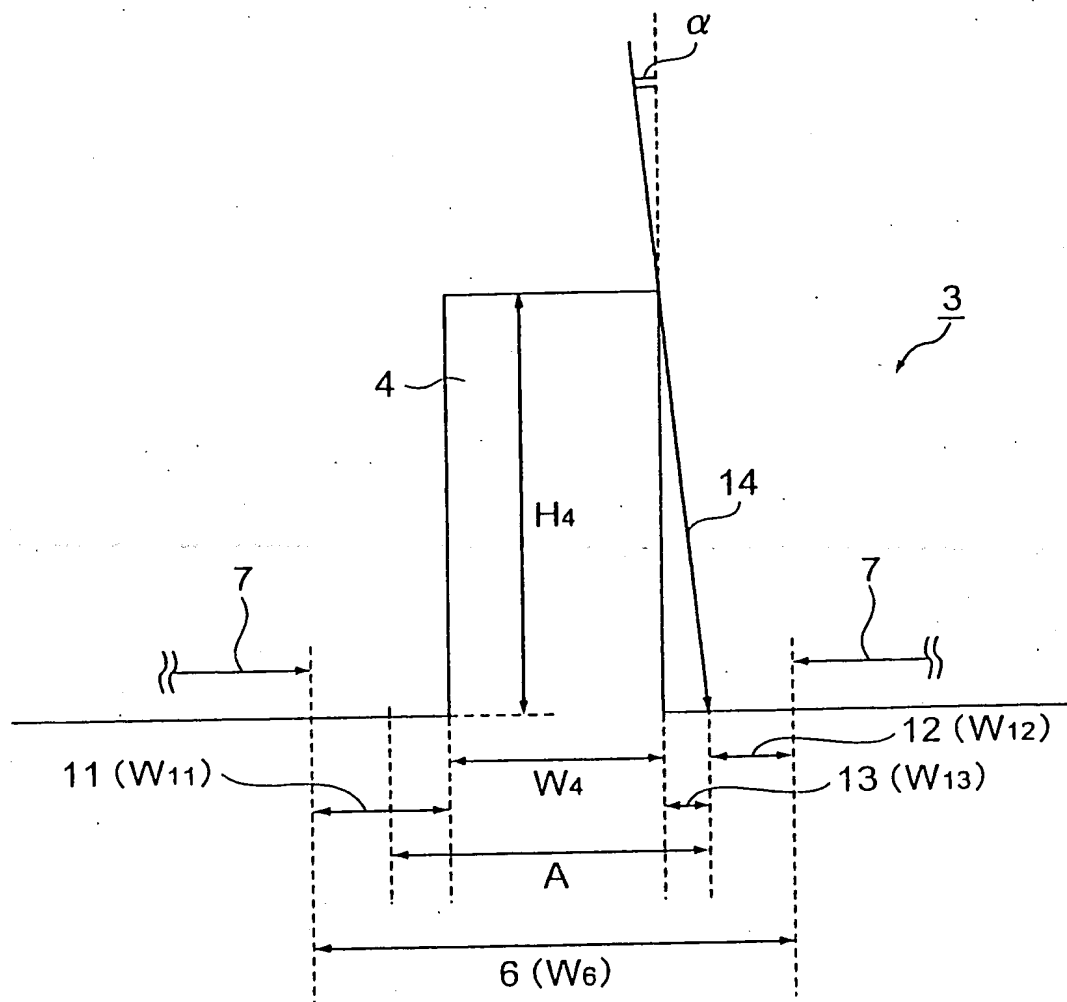


FIG. 9

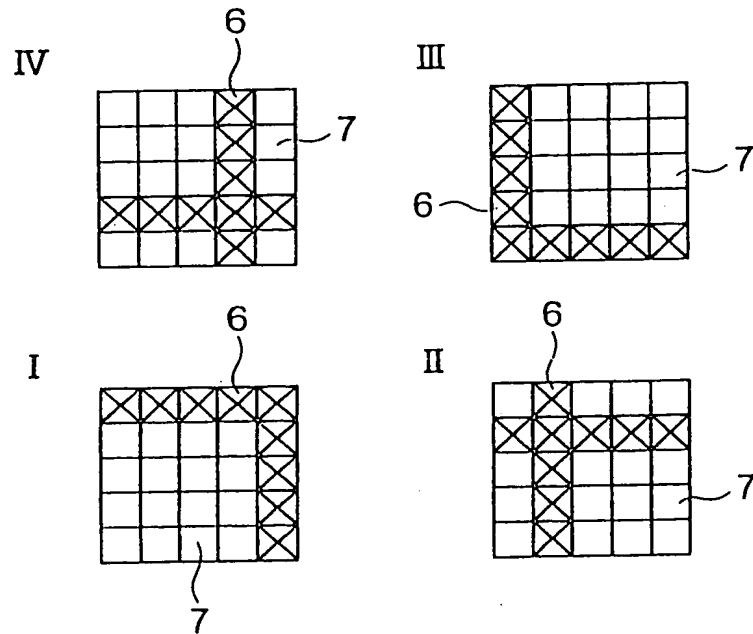


FIG. 10

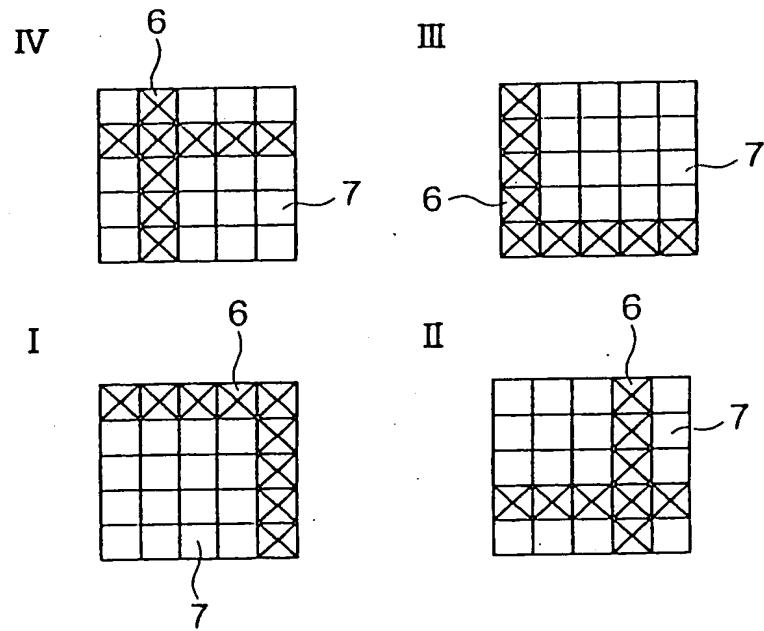


FIG. 11

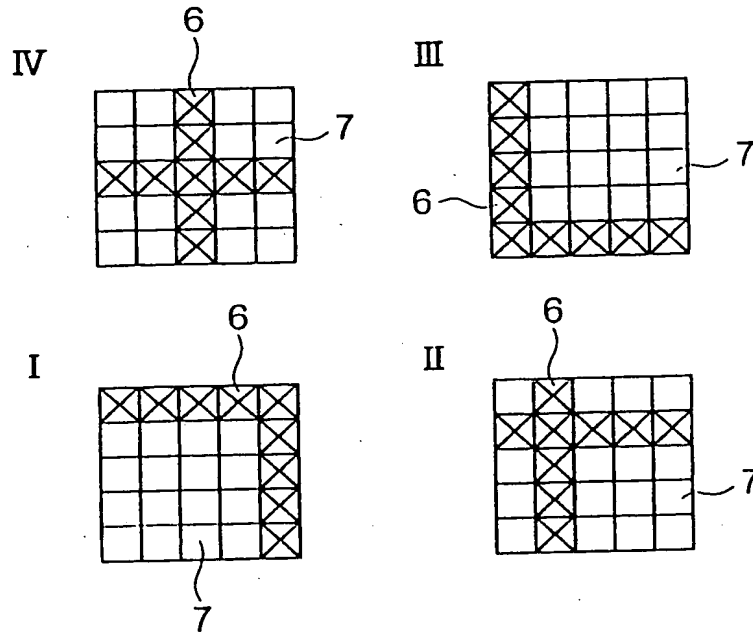


FIG. 12

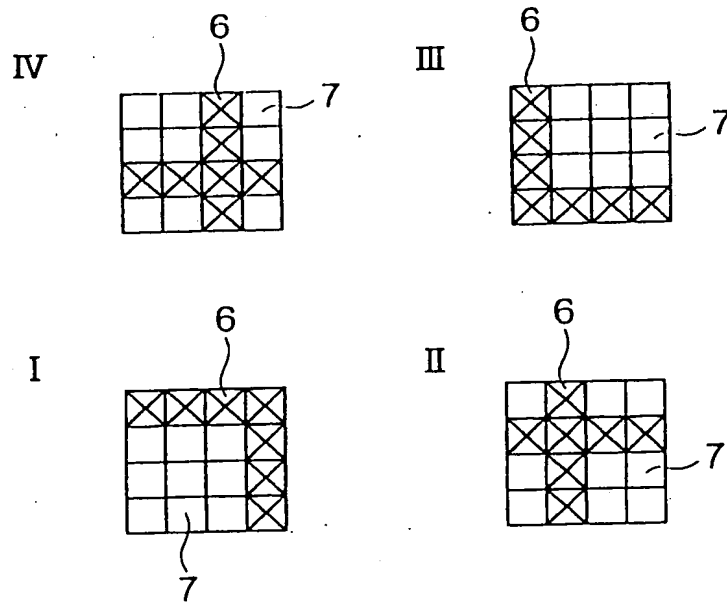


FIG. 13

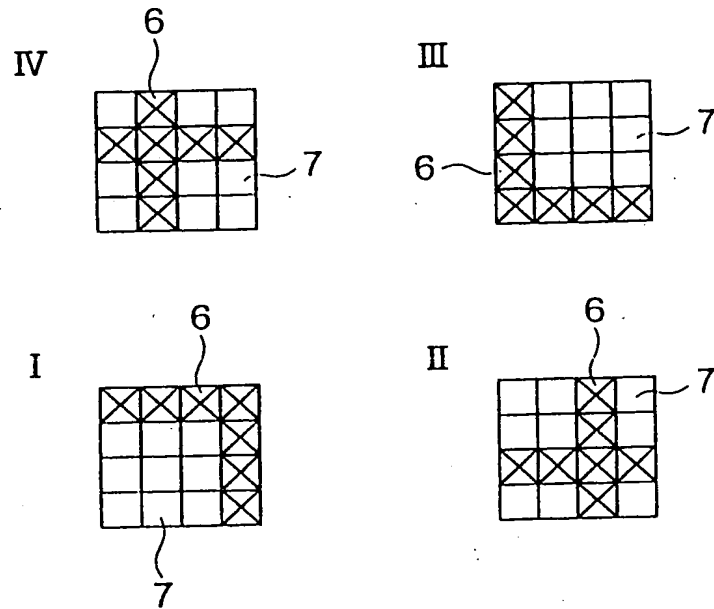
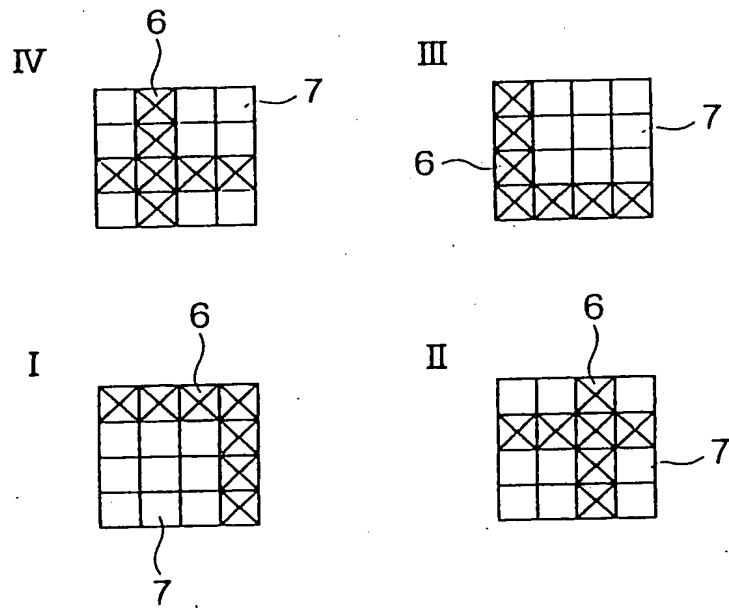


FIG. 14



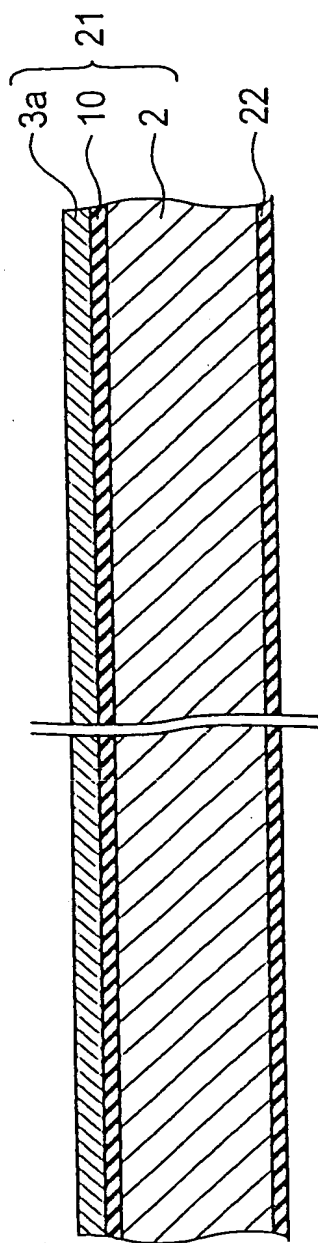


FIG. 15A

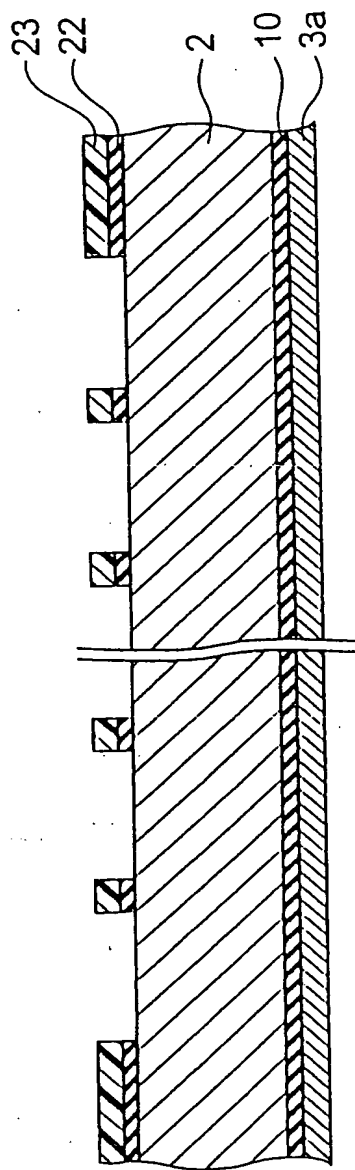


FIG. 15B

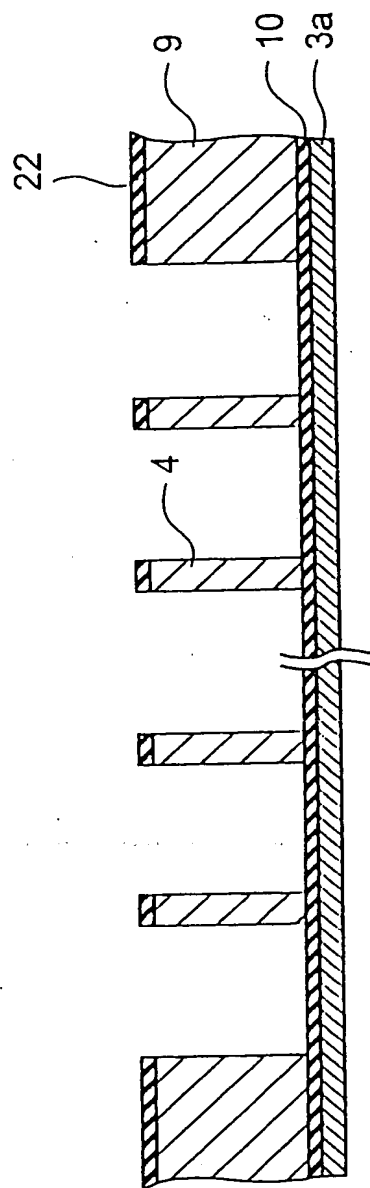


FIG. 15C

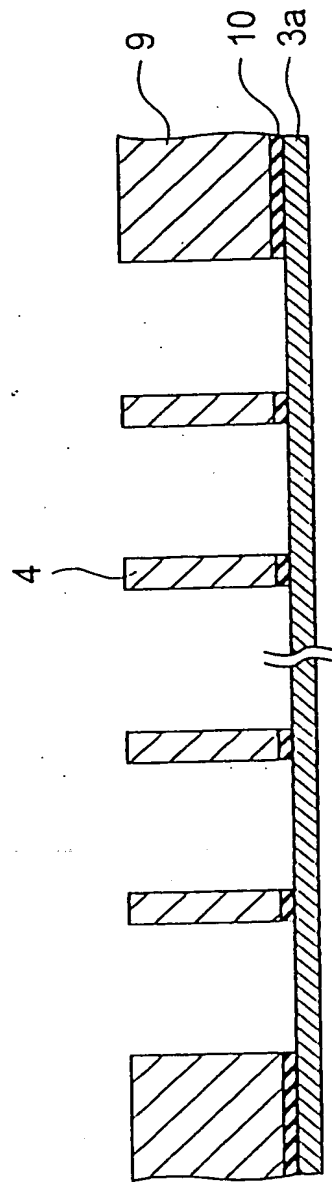


FIG. 15D

FIG. 15E

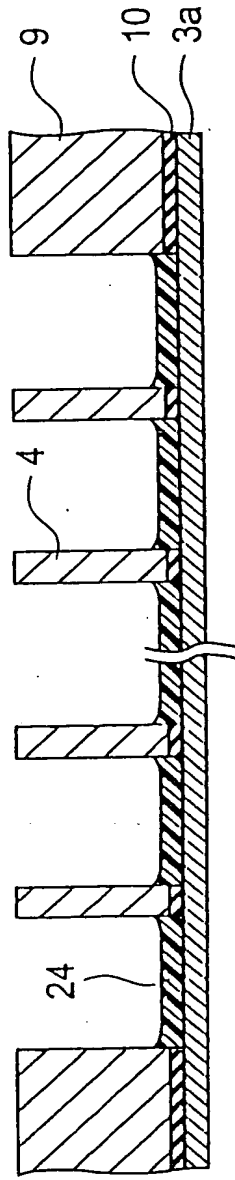


FIG. 15F

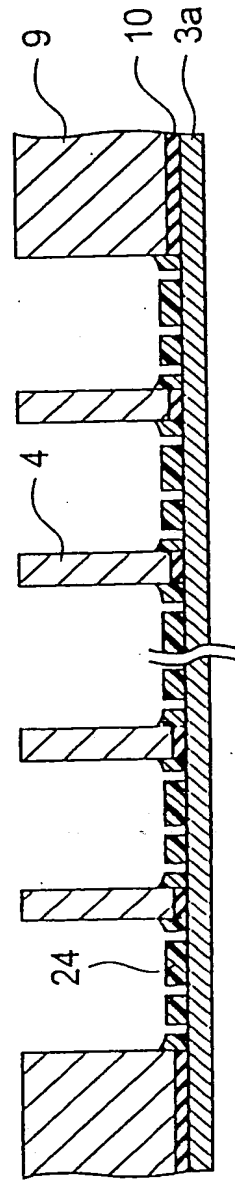


FIG. 16

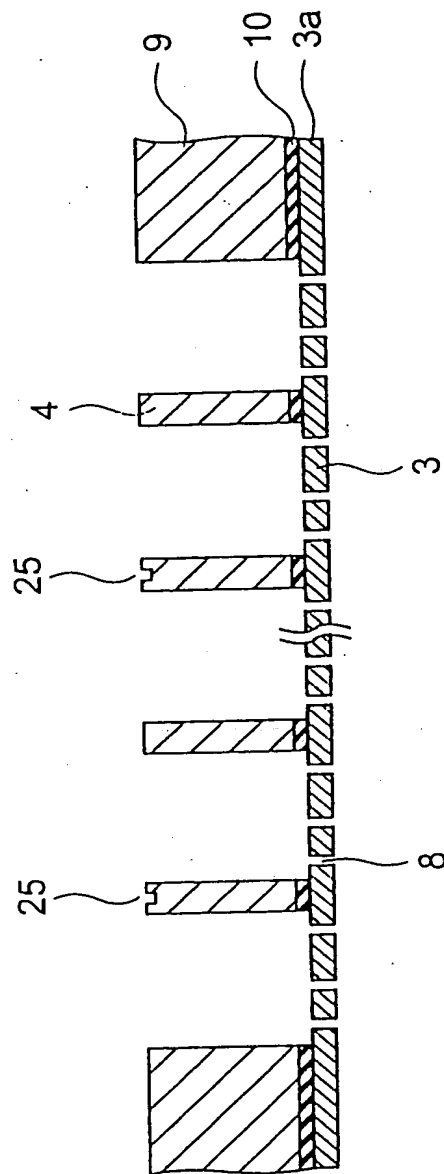


FIG. 17

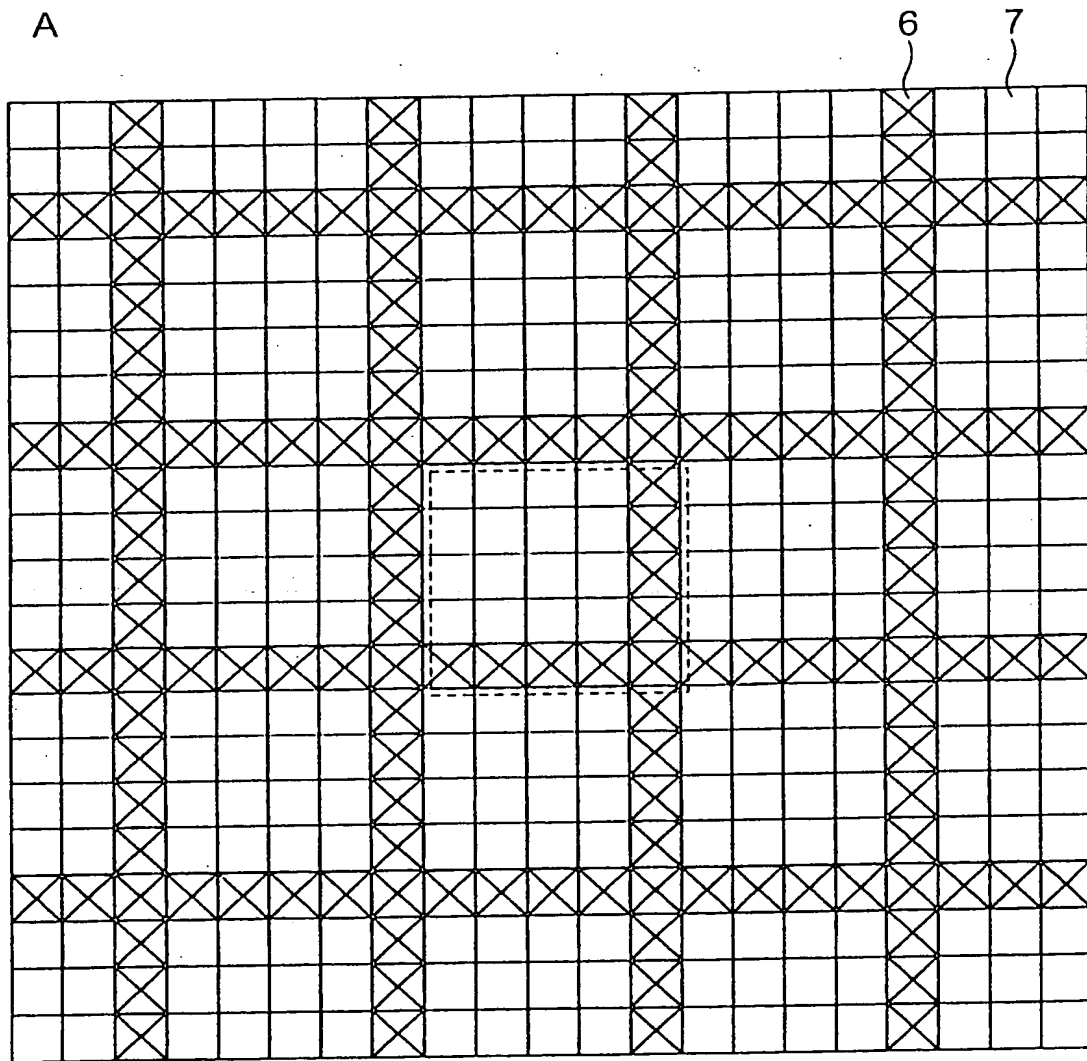


FIG. 18

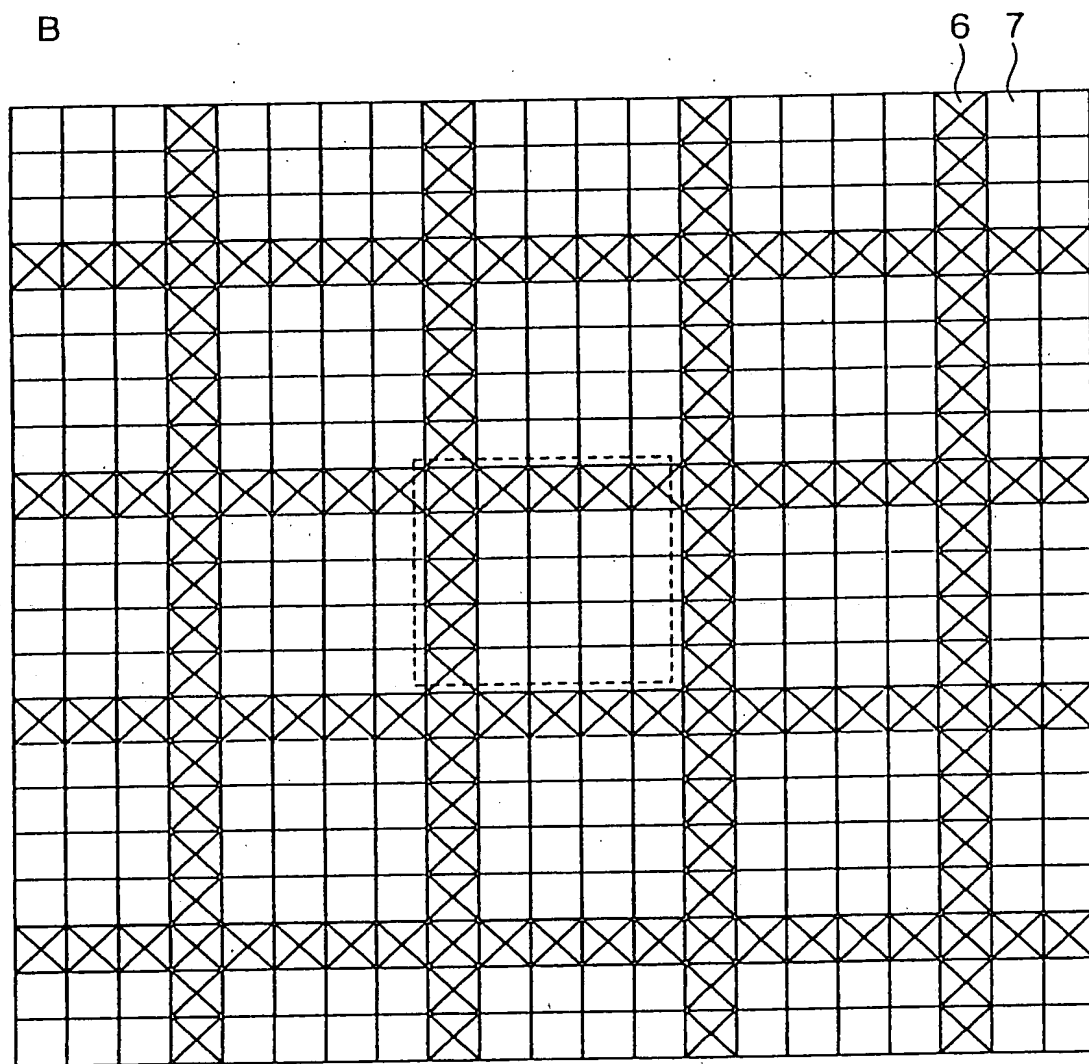


FIG. 19

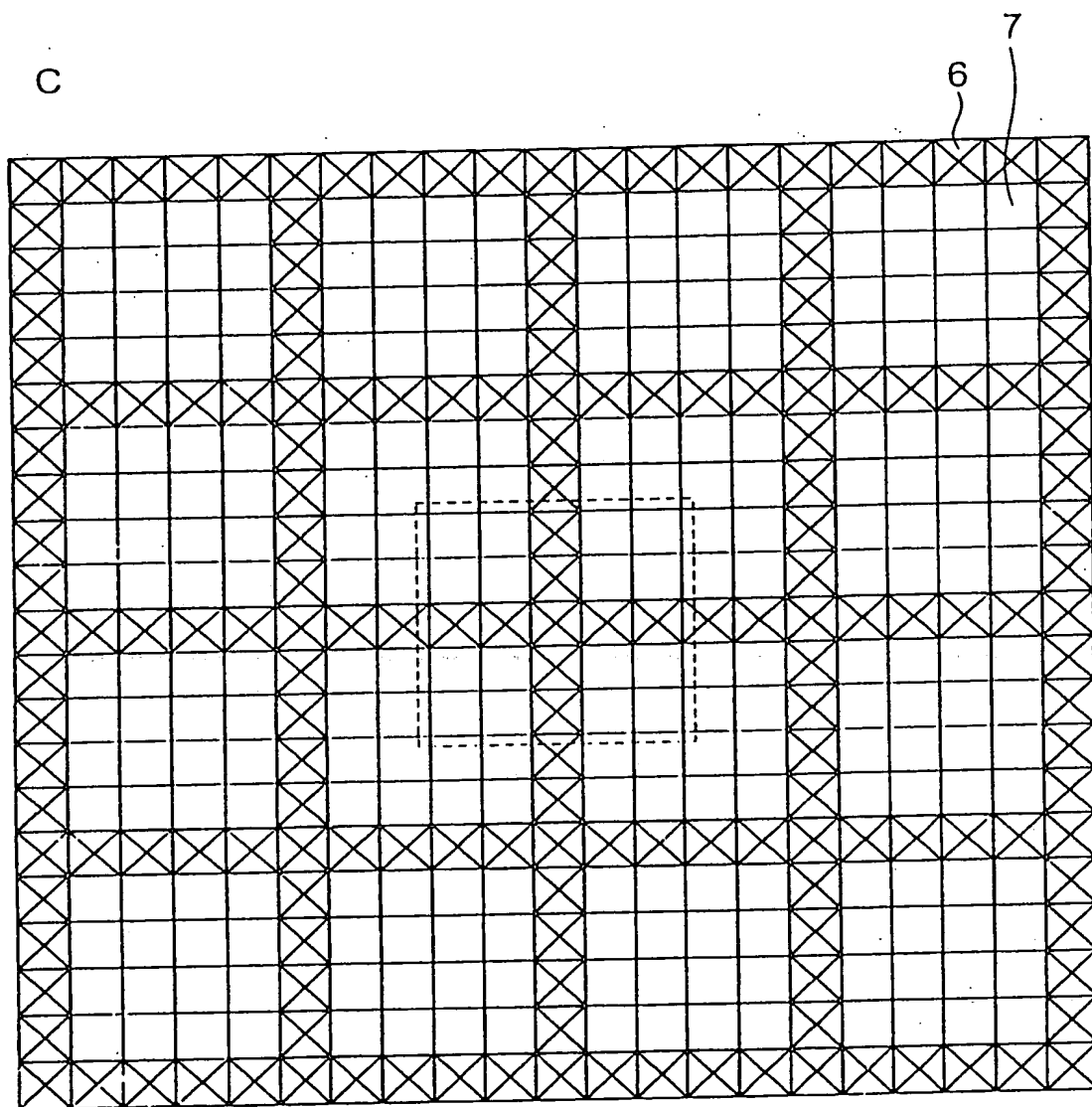


FIG. 20

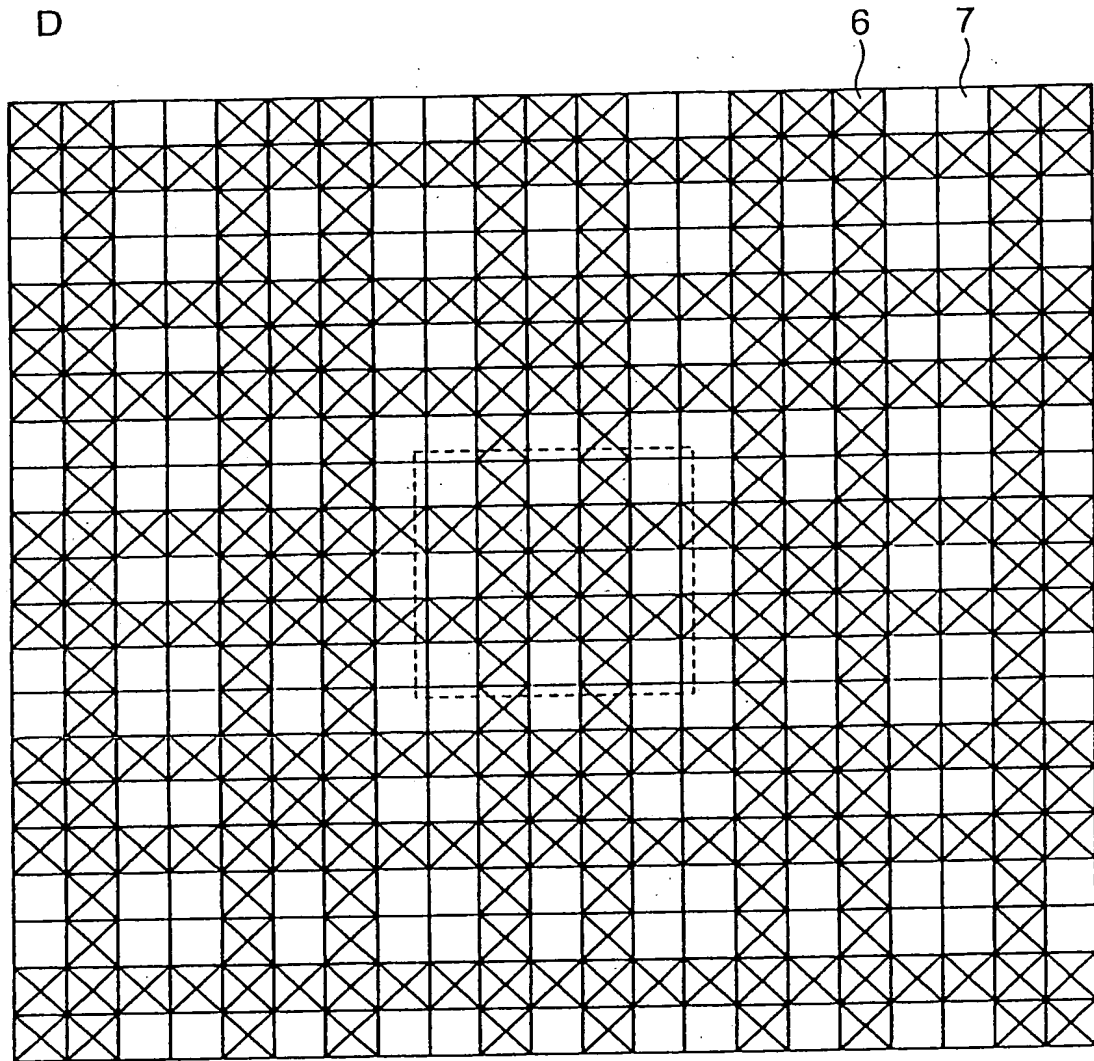


FIG. 21

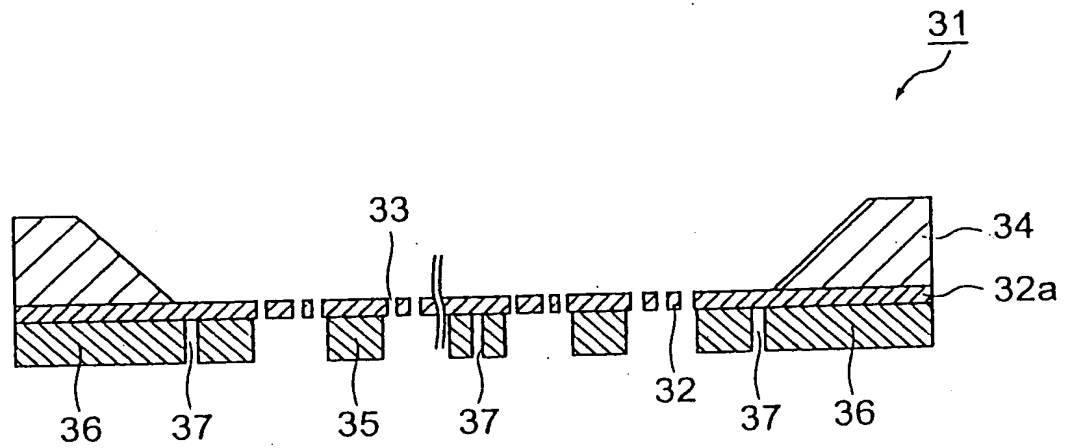


FIG. 22

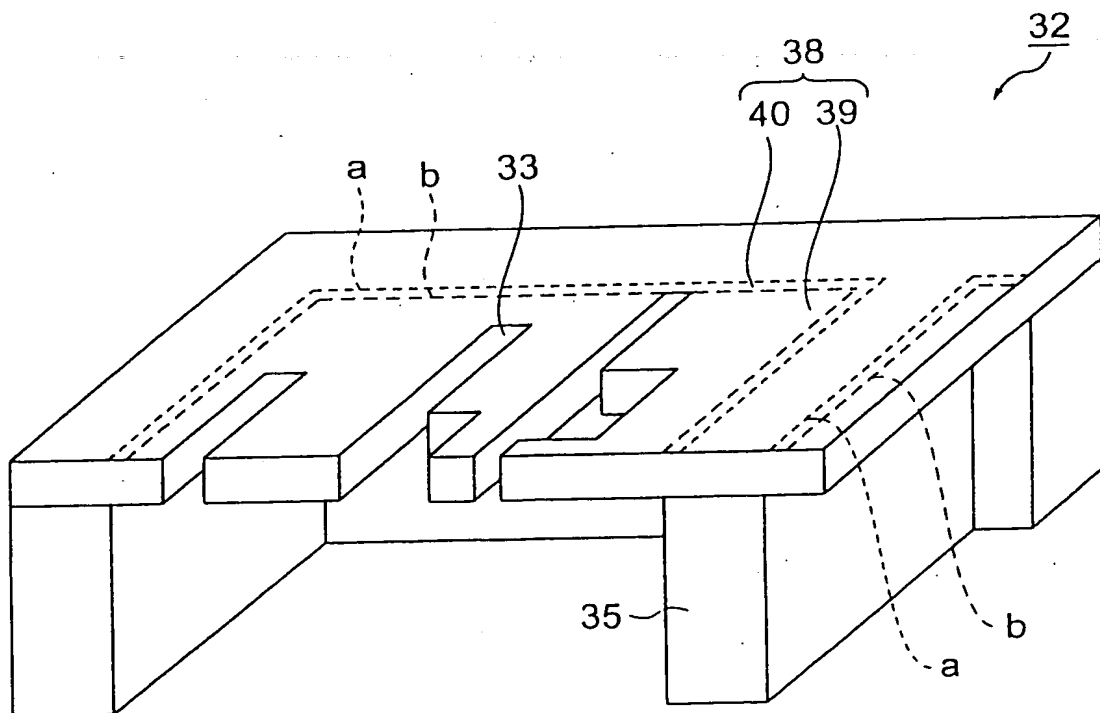


FIG. 23

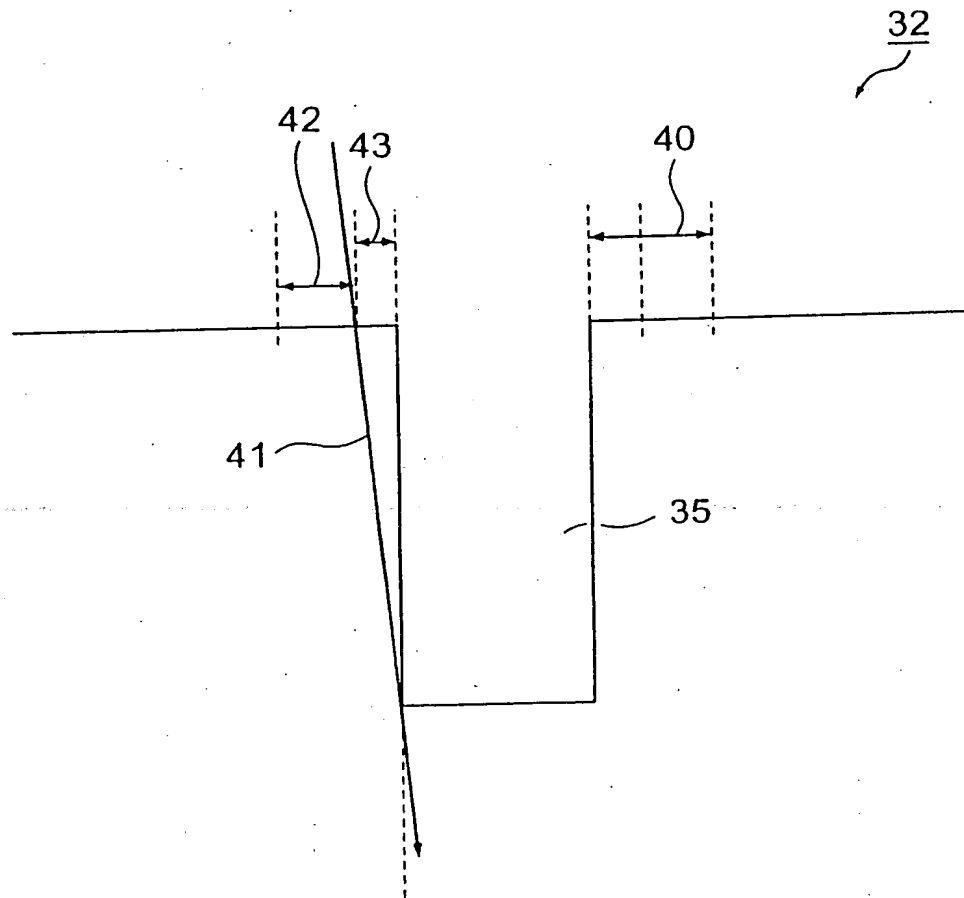


FIG. 24A

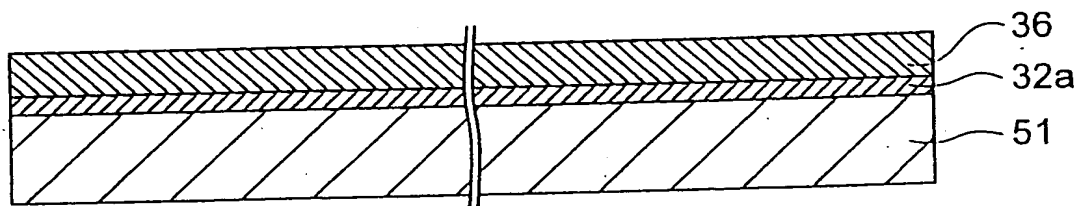


FIG. 24B

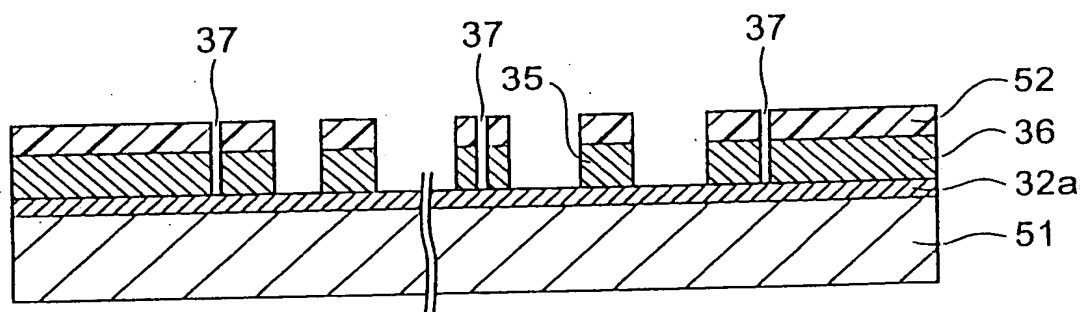


FIG. 24C

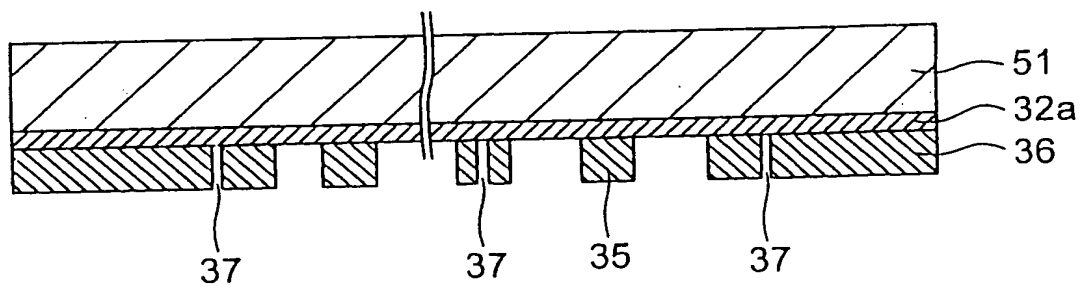


FIG. 24D

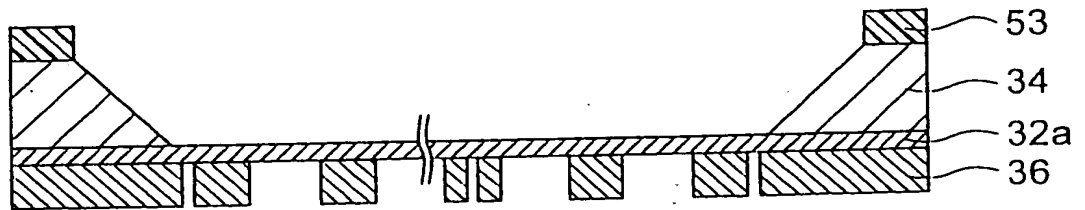


FIG. 24E

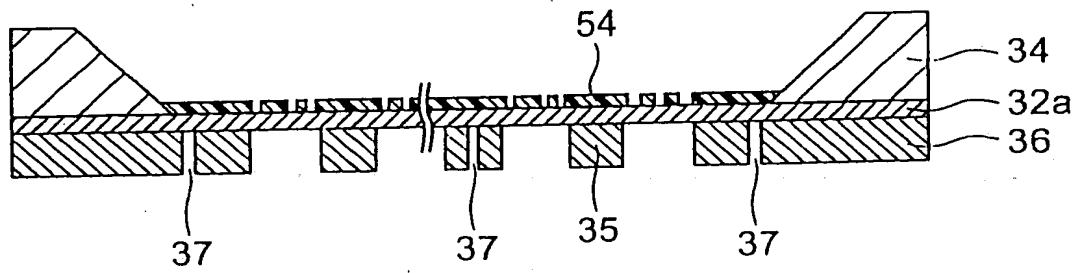


FIG. 24F

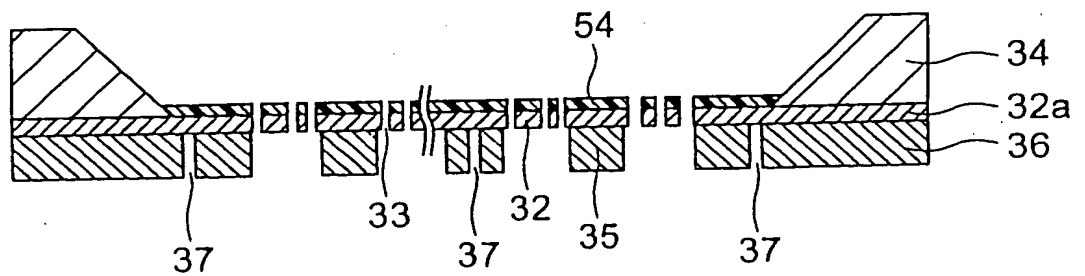


FIG. 25A

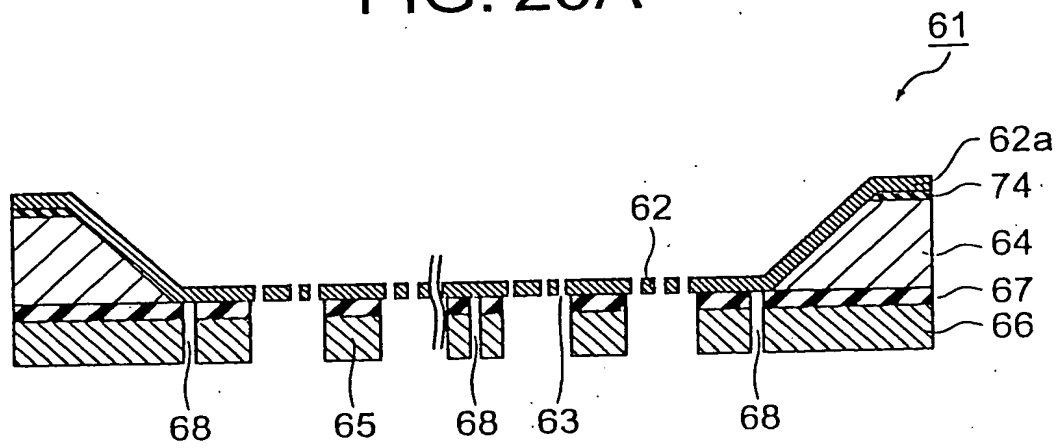


FIG. 25B

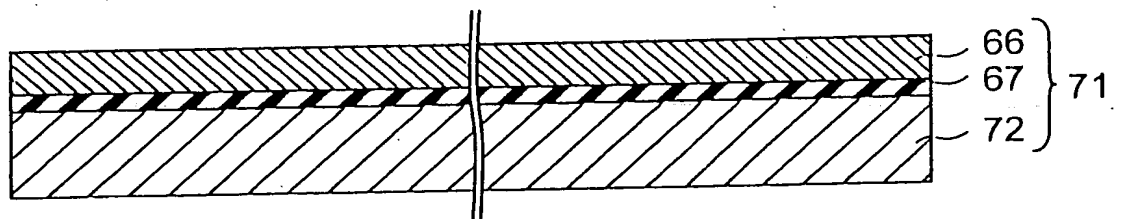


FIG. 25C

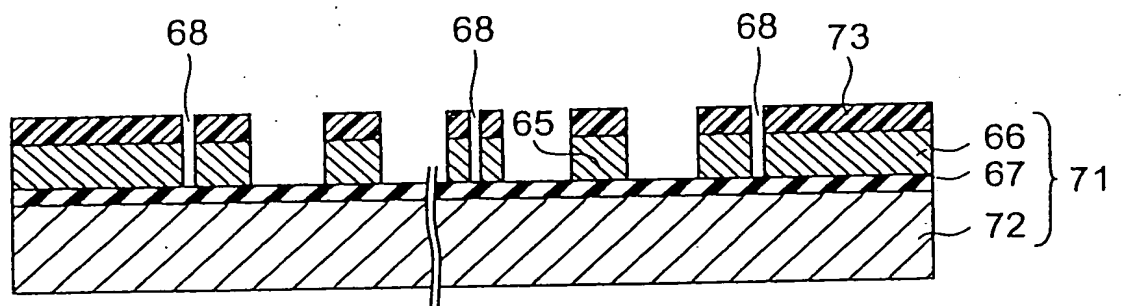


FIG. 25D

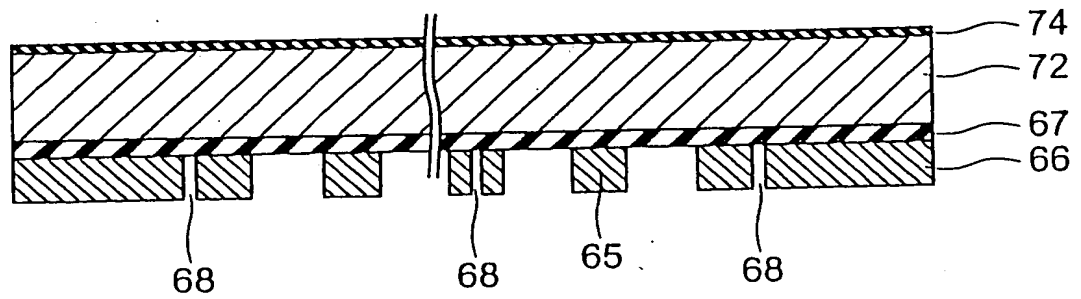


FIG. 25E

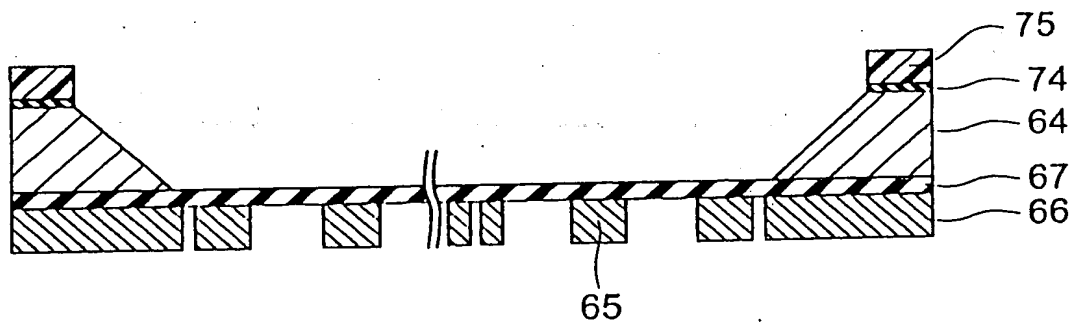


FIG. 25F

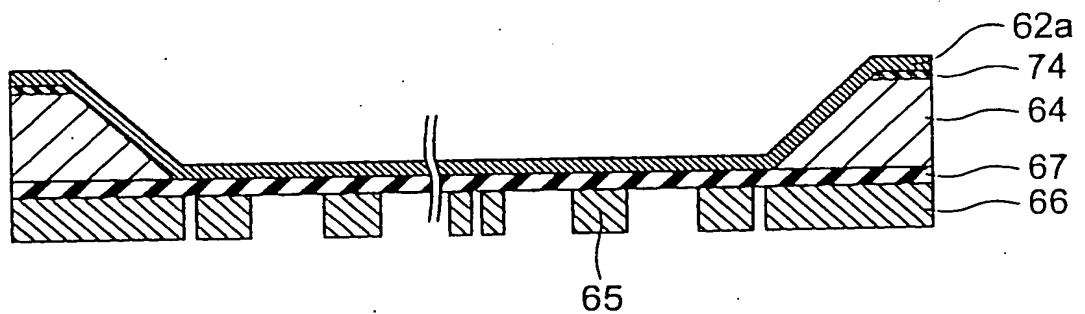


FIG. 25G

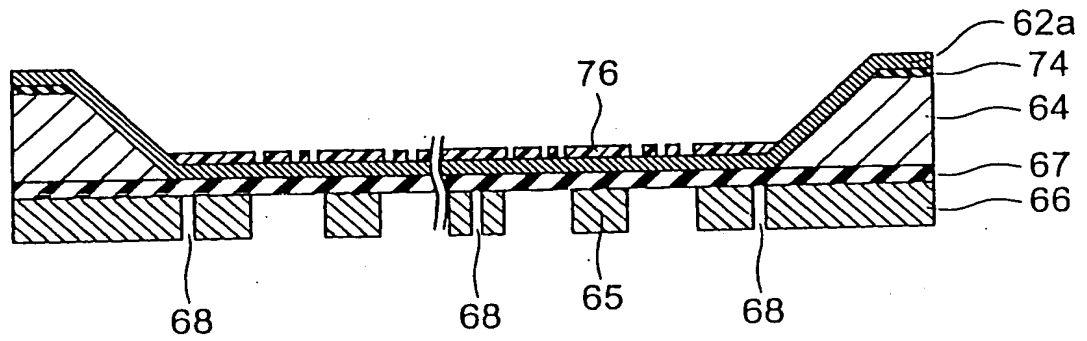


FIG. 25H

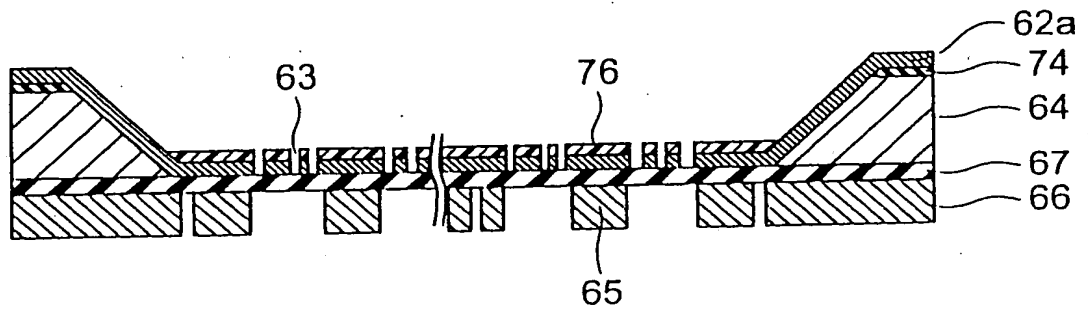
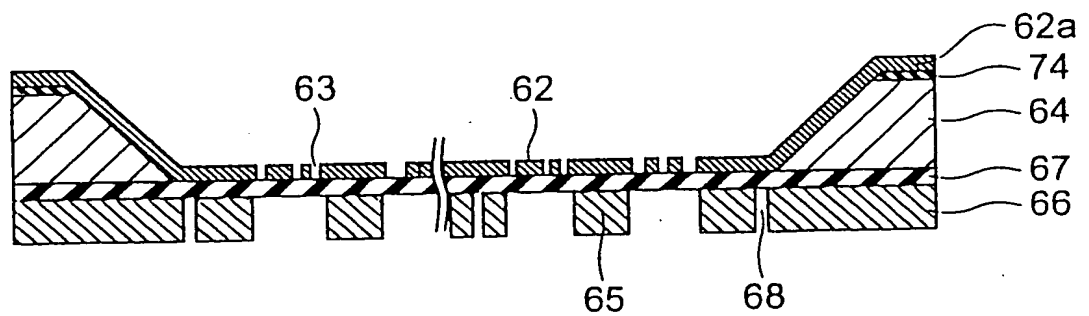


FIG. 25I



LIST OF REFERENCES

- 1, 31, 61, 101... stencil mask
- 2, 51, 72... silicon wafer
- 3, 32, 62, 102... membrane
- 3a, 32a, 62a, 102a... membrane formation layer
- 4, 35, 65... strut
- 5, 38... membrane divided region
- 6... strut zone
- 7, 39... pattern region
- 8, 33, 63, 103... hole
- 9, 34, 64, 104... frame
- 10, 67... silicon oxide film
- 11, 40... skirt
- 12, 42... margin
- 13, 43... blank
- 14, 41, 107... electron beam
- 21, 71... SOI wafer
- 22, 74... protective film
- 23, 24, 52 to 54, 73, 75, 76, 106... resist
- 25, 37, 68... alignment mark
- 36, 66... supporting layer
- 105... wafer